Name of the faculty: L.PADMALATHA

Department: Electronics and Communication Engineering
College: Seshadri Rao Gudlavalleru Engineering College
Phone No: -----
Mobile: 9440133682/9182129433
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Address for Communication

W/o M.Vinay, D.No. 4-124,
Opp.Milk chilling plant,,
Gudlavalleru, Krishna District, A.P – 521356.

Academic Qualifications

S. No	Name of the Degree *(Starting from Ph.D to 10th Class)*	University	Percentage of Marks	Specialization	Year of Pass
1	Ph.D	Acharya Nagarjuna University, Guntur	----	ECE	Pursuing
2	M.Tech	JNTU, Hyderabad	70.5	Digital Electronics and Communication Systems	2007
3	B.E	Madurai Kamaraj University, Madurai	71.4	ECE	2002
4	Diploma	SBTET, Hyderabad	65.9	DECE	1998
5	SSC	Board of Secondary Education	73.8	--	1995

Professional Experience

S. No	Designation	Institution Name	Working Period	
			From	To
1	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	01-12-2015	Till date
2	Associate Professor	Gudlavalleru Engineering College, Gudlavalleru	01-07-2012	29-10-2015
3	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	22-06-2007	30-06-2012
4	Assistant Professor	Sasi Institute of Technology & Engineering, Tadepalligudem	27-06-2006	30-05-2007
5	Associate Lecturer	Sir C.R.R.Polytechnic, Eluru	18-08-2003	20-08-2004

Professional Body Membership

S. No.	Name of the Professional body	Membership Number
1	Member of Institution of Engineers(India)	M-150336-1

Papers Published in Journals No. : 11

1. Sk.Shama, L.Padmalatha, Performance comparison of Image fusion using Singular Value Decomposition, International Journal of Innovative Research in Science, Engineering and Technology, September 2015, Vol. 4, Issue 9, pages: 8065 – 8073.
2. Sony Polumata, L.Padmalatha, Latent Fingerprint Matching based on Texture Descriptor using Hough Transform, International Journal of Computer Applications, November 2014, Vol.106, No.17, ISSN 0975–8887, Pages:37-40.
3. Ch.DivyaSusmitha, L.Padmalatha, Context Dependent Logo Detection and Recognition based on Context Dependent Similarity Kernel, International Journal of Computer Applications, November 2014, Vol.106, No.11, ISSN 0975–8887, Pages:45-47.
4. Sanjeev.P, L.Padmalatha,, Y.GowriSankar, ADIOS Based Data Transfer Protocol for PLC CPU & ECM5700, International Journal of Scientific Engineering and Technology Research, December 2013, Vol.02, Issue 19, ISSN 2319-8885, Pages: 2169-2173.
5. B.Sudarshan, L.Padmalatha, An Integer Wavelet Transformation based Adaptive Image Steganography, International Research Journal of Signal Processing, November 2013, Vol.04, Article J059, ISSN 2249-6505, pp.185-185.
6. S.Ravindra, L.Padmalatha, Color Recovery and Demosaicking for CFA color images, Color Recovery and Demosaicking for CFA color images, International Research Journal of Signal Processing, November 2013, Vol.04, Article K11313, ISSN 2249-6505, pp.186-189.
7. D.Joseph Sunder Singh, L.Padmalatha, Development of HTTP server for Remote Data Monitoring and Recording System, International Journal of Computers and Technology, October 2013, Vol.11, Issue 4, ISSN 2277-3061, pp. 2440-2445.
8. G.Kusuma, L.Padmalatha, Noise Cancellation Using Adaptive Weighting Based on Sparse RLS Algorithm, International Journal of VLSI &Signal Processing Applications (IJVSPA), December 2012, Vol. 2, Issue 5, ISSN 2231-3133, pp. 376-381.
9. D.Rakesh, L.Padmalatha, Pattern Matching Algorithm Using Filter Engine and Exact Matching Engine, International Journal of Engineering Research & Technology (IJERT), September 2012, Vol. 1, Issue 7, ISSN: 2278-0181.
10. D.Manga Rathnam, L.Padmalatha, Comparison of Reversible Data hiding Techniques for Digital images, International Journal of Engineering Research & Technology (IJERT), August 2012, Vol. 1, Issue 6, ISSN: 2278-0181.
11. L.Padmalatha, Design of Edge Detection Technique Using FPGA (Field Programmable Gate Array) and DSP (Digital Signal Processor), VSRD International Journal of Electrical, Electronics and Communication Engineering, June 2012, Vol. 2, Issue 6, ISSN: 2231-3346, 346 – 352.

Papers Published in Conferences No. : 03

1. Published a paper titled "Curved spike Fractal Antenna with DGS for GPS, Wi-Fi and Wireless Communication Applications" at 3rd International Conference on Smart Generation Computing, Communication and Networking (SMART GENCON) organized by Ghousia College of Engineering in Hybrid mode and was technically Co-sponsored by IEEE Bangalore Section during 29th – 31st December 2023.
2. Published a paper titled "IoT based Health Monitoring System (IHMS)" at 4th International Conference on Machines and Mechanisms (iNaCoMM 2019) IIT Mandi during 5-7, December 2019, pp.665-670.

3. Published a paper titled " Effective Algorithm For Reversible Data Hiding In Digital Images" in International Conference on Innovations   in Electronics & Communication Engineering (ICIECE-2012) organized by GNIT Hyderabad during 20-21, July 2012, pp. 70 – 74.

Workshops Organized No. : 05

1. Organized "YODHA- A National level Project Expo" to enhance project development skills of students on 4th February, 2015.

2. Organized a seminar on "Importance of Education & Career Guidance" on the occasion of National Education Day i.e., on 11th November 2014 for II B.Tech ECE students.

3. Organized a guest lecture on "Current trends in Control & Guidance of Aerospace Vehicles" for III and IV B.Tech ECE students on the occassion of 47th Engineer's day celebrations i.e., on 15th September 2014.

4. Organized a one day workshop on "Microwave devices::Hands on Experience" for IV B.Tech ECE Students on 4th August 2014.

5. Organized a Three day National Workshop on "Theory and Applications of Intelligent Signal Processing" for the faculty, Ph.D scholars and PG students during 28th February – 2nd March 2014.

Workshops/Conferences/Seminars/FDPs Attended No. : 48

2

1. Attended one week AICTE Recognized online FDP on " IoT and Embedded Systems" organized by the National Institute of Technical Teachers training and Research, Chandigarh during 23rd – 27th September 2024.

2. Attended one week online FDP on "Semiconductor Devices, Modeling & Circuit Technology" organized by the dept. of ECE, Lendi Institute of Engineering & Technology during 19th – 24th august 2024.

3. Attended Two days online workshop on " Design of RF & Microwave Circuits and Applications" organized by Dept. of ECE, VNR Vignana Jyotho Institute of Engineering and Technology, Hyderabad during 3rd – 4th May 2024.

4. Attended a One Week International Faculty Development Programme on "IoT, Industry 4.0 and Stress Management" (Virtual Mode) organized by the Department of IETE Vijayawada center from 17-07-2023 to 22-07-2023.

5. Attended One-Week Faculty Development Program on "MIMO Antennas for 5G Communications", through Online mode organized by Department of ECE and IETE Chapter, Kallam Haranadhareddy Institute of Technology, Guntur, from 26th June 2023 to 30th June 2023.

6. Attended One Week National Level Online Faculty Development Program on "Trends in Embedded Systems and IoT" organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 20 -25 June 2022.

7. Attended a Faculty Development Program on "Insights of NEP 2020 and Strategies for its Implementation" organized by Seshadri Rao Gudlavalleru Engineering College on 12th June 2022.

8. Attended a one week FDP on "Trends and Challenges in Advanced VLSI System Design" during 21-26, February 2022 organized by the dept. of ECE, SRKR Engineering College, Bhimavaram.

9. Attended an AICTE-ISTE approved orientation/refresher programme on "Python Programming for 5G Communications" held during 14-22, February 2022 organized by the MLR Institute of Technology, Hyderabad.

10. Attended an AICTE-ISTE sponsored One Week online Induction Programme on "Millimeter Wave Frequencies" during 16-21, November 2020 organized by the department of ECE, GEC, Gudlavalleru.

11. Attended an "Online Three Day Higher Education Conclave" during 15th – 17th October 2020 organized by IEEE Guntur sub section in collaboration with IEEE Education Society Chapter, Hyderabad section.

12. Attended "A National level webinar on Intellectual Property Rights & Patents – A View" during 10th - 12th August 2020 organized by the dept. of ECE, GEC, Gudlavalleru.

13. Attended a one week refresher course on "Embedded system Design" from 27th July to 1st August 2020 organized by the dept. of ECE, GEC, Gudlavalleru.
14. Attended a webinar on "Advanced Antenna Design for Emerging Wireless Applications" on 1st August 2020 organized by the dept. of ECE, Vidya Jyothi Institute of Technology, Hyderabad.
15. Attended a National level online FDP on "Cyber Security" during 22-26, July 2020 organized the dept. of CSE and IT in association with CysberPsy, Gudlavalleru Engg. College, Gudlavalleru.
16. Attended a webinar on "Impact of COVID on Engineering Education & Way forward" organized by ISTE chapter, Gudlavalleru Engineering College, Gudlavalleru on 9th June 2020.
17. Attended One week International online Knowledge Development Program on "Challenges and advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View" during 8-13, June 2020 organized by the dept. of ECE, Gudlavalleru Engineering College, Gudlavalleru.
18. Attended a webinar on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" on 25th May 2020 organized by the dept. of ECE, Gudlavalleru Engineering College, Gudlavalleru.
19. Attended a Five Day online FDP on "Artificial Intelligence" during 22-26, May 2020 organized by Gayatri Vidya Parishad College for Degree and PG courses (A) Engineering and Technology Program, Vizag in collaboration with Brain O Vision.
20. Attended one week online FDP on "Advanced Antenna Design using HFSS" during 12-17, May 2020 organized by Santhiram Engineering College, Nandyal, Kurnool dist.
21. Attended a webinar on "Design of Antennas for Defense Applications" on 15/05/2020 organized by Vardhaman Engineering College, Hyderabad in associaction with IEEE Hyderabad section MTT chapter and IEEE VCE SB.
22. Attended Two Day online FDP on "Microstrip Antennas for Advanced Wireless Communications" during 15-16, May 2020 organized by the dept.of ECE, Kallam Haranadh Reddy Institute of Technology, Guntur.
23. Attended a one day workshop on " eSim - a First course in IoT series for Teachers" held at Ramachandra college of Engineering, Vatluru on 21st September 2019 organized by Teaching Learning Centre ICT at IIT Bombay, funded by the Pandit Madan Mohan Malaviya National Mission on Teachers and Teaching (PMMMNMTT), MHRD, Govt. of India.
24. Attended a one day workshop on "NPTEL Awareness Program" jointly organized by Gudlavalleru Engineering College and IIT, Madras on 6th December 2018.
25. Attended one week faculty development program on "Communications and signal processing" organized by the department of ECE, GEC, Gudlavalleru during 29th October to 3rd November 2018.
26. Attended one week MHRD, Govt. of India sponsored faculty development program on "Optimization techniques in Antenna Design" organized by the E&ICT Academy, NIT, Warangal in association with dept. of ECE, NIT, Warangal during 25 – 30, June 2018.
27. Attended one week National level Faculty Development Program on "Signal and image Processing using Lab VIEW' organized by signal processing research group of Dept., of ECE, GEC, Gudlavalleru during 13 – 18, November 2017.
28. Attended a five day faculty development program on " Introduction and thorough working with HFSS for beginners on Design and Anlaysis of Antennas" conducted by the department of ECE, Usharama college of Engineering and Technology during 6 – 10, June 2017.
29. Attended one week faculty development program on "Hands on Experience in Signal Processing Applications" organized by the department of ECE, GEC, Gudlavalleru during 7-12, November 2016.
30. Attended a Five day workshop on " Design and Simulation of Antennas & Microwave Devices using ANSYS HFSS" organized by the department of ECE under TEQIP – II, Shri Vishnu Engineering College for Women (Autonomous), Bhimavaram during 3 – 7, October 2016.
31. Attended a two day workshop on " Research Methodologies and Guidelines to Paper Publication" organized by the department of EEE under TEQIP-II S.C.1.2 at VR Siddartha Engineering College (Autonomous), Vijayawada during 23 – 24, September 2016.

32. Attended a one day workshop on "Ultra Low power communication using CC430 Microcontroller: Hands on experience" organized by the dept. of ECE, GEC on 7th February 2015.
33. Attended a Two week workshop on "Control Systems" organized by IIT, Kharagpur at GEC, Gudlavalleru during 2– 12, December 2014.
34. Attended one day seminar on "Research in Signal & Image Processing on FPGAs and Embedded platforms" organized by The Institution of Engineers (India), Vijayawada Local Centre, Vijayawada on 22nd November 2014.
35. Attended a two day National workshop on " Recent trends and research scope on Signal Processing and Communications" at MIC College of Technology, Kanchikacherla during 27-28, August 2014.
36. Attended a one day workshop on "Speech Processing: Current Challenges and Hands on   Experience" organized by the dept. of ECE, GEC on 23rd August 2014.
37. Attended a two day National workshop on "Advances in Signal & Image Processing" at University College of Engineering, JNTUK under TEQUIP – II, Kakinada during 25-26, April 2014.
38. Attended a Three day National workshop on "Theory and Applications of Intelligent Signal Processing" organized by the Dept. of ECE, GEC, Gudlavalleru during 28th February – 2nd March, 2014.
39. Attended a Ten days workshop on "Signals and Systems" organized by IIT, Kharagpur at GEC, Gudlavalleru during 2– 12, January 2014.
40. Attended a Two day workshop on "Issues in Integrated Circuits (ICs) Design" organized by the Dept. of ECE, GEC, Gudlavalleru during 13– 14, December 2013.
41. Attended a Two – Week workshop on "Analog Electronics" organized by ISTE and Conducted by IIT, Kharagpur at GEC, Gudlavalleru during 4-14, June 2013.
42. Attended a two – day ISTE workshop on "Aakash for Education" conducted by IIT, Bombay during 10 – 11, November 2012. It is held under MHRD at GEC as Remote Center.
43. Attended a two – day faculty development program on "VLSI Design & Embedded Systems" conducted by Dept. of ECE, GEC, Gudlavalleru during 7-8, July 2012.
44. Attended a Two day workshop on "Recent Trends in Microwave Engineering& Fiber Optic Communications" at PVPSIT, Kanuru, Vijayawada during 9-10, December 2011.
45. Attended "One week refresher course on Pulse & Digital Circuits" (Theory& Lab practice) organized by the Dept. of ECE, GEC, Gudlavalleru during 21-26 November 2011.
46. Attended a one day workshop on "Analog, Digital and Mixed signal VLSI Design" organized by the Department of Electronics and Communication Engineering on 15th October 2011.
47. Attended an AICTE Sponsored Staff Development Program on "Current Trends in Biometrics" organized by the Department of Information Technology during 2nd – 14th November, 2009.
48. Attended a "Two – Day National Workshop on Pedagogy" organized by PG Department of Business Administration in association with Association of Indian Management Schools (AIMS) during 30th – 31st January, 2009.

Certifications/Training Programs Attended No. : 20

1. Cerified by Curtin University for successfully completing edX courses on IOT1x: Introduction to the Internet of Thongs (IoT), IOT2x:IoT Sensors and Devices and IoT Networks and Protocols on 12th may 2024.
2. Got NPTEL online certification for successfully completing the course "Analog Electronic Circuits" with Elite grade conducted by IIT Madras during July - October 2022.
3. Attended an AICTE-ISTE sponsored one week online induction programme on "Millimeter Wave Frequencies" (Phase – III) organized by the dept. of ECE, GEC during 17-24, August 2021.

4. Got NPTEL online certification for successfully completing the course "Microelectronics: Devices to Circuits" with Elite plus Silver grade conducted by IIT Roorkee during September - December 2020.

5. Attended and completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Wearable Devices" during 4th to 8th January 2021 at Vignan Institute of Technology and Science.

6. Attended and completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Internet of Things" during 7th to 11th December at GEC.

7. Attended an Online Faculty Development Program on "Universal Human Values in the context of Student Induction Program" during 2nd – 7th November 2020 sponsored by TEQUIP-III and organized by NIT Warangal.

8. Attended an AICTE sponsored Online Short Term Training Program on "Blockchain Architecture Design and Use Cases" Phase – I from 10th – 15th, August 2020 organized by the dept. of ECE, GEC, Gudlavalleru.

9. Got NPTEL online certification for successfully completing the course "Digital Circuits" with Elite plus Silver grade conducted by IIT Kharagpur during July - October 2019.

10. Attended a two day training program on "Design and Development of Digital Circuits using VLSI Tools, organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 6-7, December 2018.

11. Got NPTEL online certification for successfully completing the course "Fundamentals of Semiconductor devices" with Elite grade conducted by IISc, Banganlore during January-April 2019.

12. Got NPTEL online certification for successfully completing the course "Fundamentals of electronic materials and devices" with Elite grade conducted by IIT, Madras during January-April 2019.

13. Got NPTEL online certification for successfully completing the course "Semiconductor Devices and Circuits" with Elite grade conducted by IIT, Madras during July-October 2018.

14. Got NPTEL online certification for successfully completing the course "Basic Electronics" with Elite grade conducted by IIT, Bombay during January-April 2018.

15. Attended a Two day faculty training program on "Advances in VLSI Design and Embedded Systems" during 6th – 7th November 2017, organized by the Dept. of ECE, GEC, Gudlavalleru.

16. Attended a two day staff training program on "MATLAB tools for Engineers – Hands on Experience" held in GEC, Gudlavalleru during 10 – 11, January 2017.

17. Attended a Two day staff training program on "Simulation of Linear & Digital Integrated Circuits, Microcontroller based Applications using NI Multisim" during 4th – 5th November 2016, organized by the Dept. of ECE, GEC, Gudlavalleru.

18. Attended three day training program on "Overview of Latest Telecom Technologies" conducted by the department of ECE, GEC in association with BSNL (A government of India Enterprise) during 18-20, December 2014.

19. Attended a two – day training program on "MATLAB, Simulink & Related tool boxes for Engineering Education" organised by the departments of EEE & ECE during 3- 4, April 2013.

20. Attended a Five Day Training Program on "Mentor Graphics (Front end and Back end) and Xilinx tools organized by the Department of Electronics & Communication Engineering, GEC, Gudlavalleru in association with IETE Vijayawada Centre and CoreEL Technologies, Bangalore during 13th – 17th September 2010.

Guest Lectures Delivered No. : 01

1. Acted as resource person for "One week refresher course on Electronic Devices and Circuits" conducted during 23-30 September 2013, organized by the Dept. of ECE, GEC.

Other Responsibilities

College Level
1. Women – Protection Cell member
2. Coordinator for Central announcements in GECFEST'14
3. Member, College Publications Committee
4. Coordinator for Central announcements in GECFEST'13

Department Level
1. Final year Academic coordinator
2. Minor/Honor degree coordinator
3. Internal examination coordinator for II B.Tech
4. Anti- ragging coordinator
5. Online feedback coordinator for III ECE
6. Prepared SDC Lab Manual (R20)
7. Coordinator for ED lab manual preparation(R17)
8. Faculty Advisor for IE (I) for (2014-15)
9. National Workshop Coordinator
10. Research Coordinator for Signal Processing group
11. ECAD Lab In-Charge
12. Internal Examination In-charge
13. Department Library In-Charge
14. Student Counselor
15. Class In – Charge
16. Project Guide (B.Tech & M.Tech)

Projects Guided
Under Graduation

S. No	Name of the project	Year
1	Detection and jamming of low probability intercept RADARs	2009
2	Velocity estimation using image histograms	2011
3	Real time moving object detection for Video Surveillance	2012
4	Automatic ambulance identification & traffic signal coordination system	2012
5	Safe trajectory path at zebra-cross and obstacle detection for blind	2014
6	Microcontroller based Multi Sensor Data Fusion	2015
7	Sensor based Efficient and Intelligent Street lighting using Zig-Bee	2015
8	IOT based Flood Alerting system	2018

9	Fingerprint based Electronic Voting System using IoT	2019
10	Adaptive Beam formation using LMS Algorithm for Smart Antenna	2020
11	RFID based Electronic Voting System using IoT	2021
12	IOT based Face recognition for home security	2022
13	Detect and Display the Subterranean failing of Cables using IoT	2023
14	Autonomous Electric Vehicle	2024

Post Graduation

S.No	Name of the project	Year
1	Performance comparison of Image Fusion using Singular Value Decomposition	2015
2	Latent Fingerprint Matching based on Texture Descriptor Using Hough Transform	2014
3	Context Dependent Logo Detection and Recognition based on Context Dependent Similarity Kernel	2014
4	An Integer Wavelet Transformation based Adaptive Image Steganography	2013
5	Color Recovery and Demosaicking for CFA color images	2013
6	ADIOS Based Data Transfer Protocol for PLC CPU & ECM5700	2012
7	Development of HTTP server for Remote Data Monitoring and Recording System	2012
8	Noise Cancellation Using Adaptive Weighting Based on Sparse RLS Algorithm	2011
9	Pattern Matching Algorithm Using Filter Engine and Exact Matching Engine	2011
10	Comparison of Reversible Data hiding Techniques for Digital images	2011

Books Published No. : 04

1. Co-author for chapter titled "IoT based Health Monitoring System" in Lecture Notes in Mechanical Engineering, Machines, Mechanics and Robotics, Springer Nature Singapore Pte Ltd, 2021, pp: 179-185 with ISBN 978-981-16-0550-5_16
2. Co-author for chapter 1 titled "Electronic Device and Circuits Laboratory" of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016.
3. Co-author for chapter 10 titled "Electronic Measurements and Instrumentation" of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016.
4. Co-author for chapter 1 titled "Electronic Device and Circuits Laboratory" of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015.

Patents Published: 02

1. A patent published in Official Journal of The Patent Office with title " A kind of Rectangular Patch Antenna using Multi-Resonant slots for Bandwidth Improvement" on 11/06/2021, Issue No.24/2021.
2. A patent published in Official Journal of The Patent Office with the title " A Smart Multi -Mode Floor Cleaning Device" on 11th September 2020, Issue No. 37/2020.

R&D and Consultancy

S. No	Project Title	Source of Funding	Duration	Role	Status
1	IoT based Health Monitoring System	Internal	1 Year	Co-investigator	Completed
2	Digitalized Smart college Bus Tracking system	S.V.Technologies, Vijayawada	3 Years	Co-investigator	Completed

Curriculum Design and Development

1. Designed the syllabus for Analog Circuits for II B.Tech ECE students for Autonomous Regulation (R20).

2. Designed the syllabus for Electronic Devices and Electronic Devices Lab for II B.Tech ECE students for Autonomous Regulation (R17).

Academic Rewards / Achievements

1. Received Certificate of Appreciation and recognized as NPTEL Discipline Star Dec 2020 from the Department of Electrical Engineering, NPTEL, IIT Madras.
   Obtained 9th Rank in Acharya Nagarjuna University Research Common Entrance Test for Part-time Ph. D admission in the year 2015.

2. Achieved 3rd Best Project award for the project entitled "Real time moving object detection for video surveillance" for the academic year (2011-2012).

3. Received an amount of Rs. 45,000/- as gratuity kind of payment for the retention of 5 Years.

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: Signature
Place: Gudlavalleru   (L.Padmalatha)